		2004/11/01 10:03	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3) or pre-charg\$3) with electric\$4	21	BRS	Q
		2004/11/01 09:57	USPAT	"5528544".PN.		BRS	ហ
		2004/11/01 09:57	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with semiconductor\$1 with memory and pre-charg\$3 with electric\$4	4	BRS	4.
		2004/11/01 10:45	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with semiconductor\$1 with memory	161	BRS	ω
	,	2004/11/01 09:51	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("5870343").PN.	N	IS&R	Ν
		2004/11/01 09:49	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	"20040186678"	,	BRS	۲
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1	1	US-PGPUB; EPO JPO; DERWENT; IBM_TDB; EPO JPO; DERWENT; IBM_TDB US-PGPUB; EPO JPO; DERWENT; IBM_TDB US-PGPUB; EPO JPO; DERWENT; IBM_TDB IBM_TDB; EPO JPO; DERWENT; IBM_TDB	US-PGPUB; EPO; 2004/11/ JPO; DERWENT; 09:59 IBM_TDB USPAT; US-PGPUB; EPO; 2004/11/ JPO; DERWENT; 10:00 IBM_TDB US-PGPUB; EPO; 2004/11/ JPO; DERWENT; 10:00 IBM_TDB US-PGPUB; EPO; 2004/11/ JPO; DERWENT; 10:01 IBM_TDB
bit adj line) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with module\$1 with electric\$4 sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with electric\$4 and isolat\$3 sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with semiconductor\$1 with semiconductor\$1 with electric\$4 and isolat\$3 with transmit\$4 sens\$3 with circuit\$1 and single with (bit-line or pre-charg\$3) with semiconductor\$1 with sens\$3 with circuit\$1 and single with (bit-line or pre-charg\$3 with (bit-line or bit adj line) with semiconductor\$1 with	miconductor\$1 with e-charg\$3) with module\$1 th electric\$4 ns\$3 with circuit\$1 and ngle with (bit-line or e-charg\$3) with e-charg\$3) with e-charg\$3) with getric\$4 and isolat\$3 ns\$3 with circuit\$1 and ngle with (bit-line or t adj line) with miconductor\$1 with miconductor\$1 with e-charg\$3) with e-charg\$3) with e-charg\$3) with miconductor\$1 with module\$1 US-PGPUB; EPO	miconductor\$1 with miconductor\$1 with miconductor\$1 with e-charg\$3) with module\$1 th electric\$4 ns\$3 with circuit\$1 and ngle with (bit-line or e-charg\$3) with e-charg\$3) with e-charg\$3) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or e-charg\$3) with e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or line) with line or lin	miconductor\$1 with miconductor\$1 with miconductor\$1 with e-charg\$3) with module\$1 th electric\$4 ns\$3 with circuit\$1 and ngle with (bit-line or e-charg\$3) with e-charg\$3) with e-charg\$3) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or e-charg\$3) with e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or e-charg\$3) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or thad line) with mory and (precharg\$3 or line) with line or lin
		; 2004/11/ 09:59 ; 2004/11/ ; 2004/11/ ; 2004/11/ ; 2004/11/	; 2004/11/ 09:59 ; 2004/11/ ; 2004/11/ ; 2004/11/ ; 2004/11/

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ts Search Text	sens\$3 with circuit\$1 and single with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3) or pre-charg\$3) with electric\$4 and logic\$4	1 702/117,186.ccls.	365/185.2,189.01,189.09,19 10 1,196,198,201,203,207,208, 210.ccls.		0 714/718-722,724.ccls.
DBs	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	l	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
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16 BRS	ф 57	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line or bit adj line) with semiconductor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 10:58		
17 BRS	227	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line or bit adj line) with data with line and semiconductor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 10:59		
18 BRS		sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line or bit adj line) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with select\$3 with bit with line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 11:01		

Туре	Hits	Search Text	DBs	Time Stamp	Comments	Error Definiti on
19 BRS	6	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line or bit adj line) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with line with bit with line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01 11:02		
20 BRS	 	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line or bit adj line) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with module\$1 with (multiplex\$3 or select\$3) with bit with line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB.	2004/11/01		

		2004/11/01 13:09	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	711/5,169.ccls.	1434	BRS	23
		2004/11/01	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line\$1 or bit adjline\$1) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with (multiplex\$3 or select\$3) with bit with line\$1	Q)	BRS	22
		2004/11/01	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line\$1 or bit adjline\$1) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with electric\$4 with with bit with line\$1 and module\$1		BRS	21
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714/718,719,720,721,722,72 4,755.ccls.	365/185.2,185.12,185.17,18 5.18,185.21,185.22,189.01, 189.09,191,196,198,201,203 ,207,208,210,230.03,233.cc	Search Text
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3 2	BRS		(365/185.2,185.12,185.17,1 85.18,185.21,185.22,189.01 ,189.09,191,196,198,201,20 3,207,208,210,230.03,233.c cls. or 714/718,719,720,721,722,72 4,755.ccls. or 711/5,169.ccls. or 702/117,186.ccls.) and (pre-charg\$3 or	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01	
$\frac{\omega}{\omega}$	BRS		(365/185.2,185.12,185.17,1 85.18,185.21,185.22,189.01 ,189.09;191,196,198,201,20 3,207,208,210,230.03,233.c cls. or 714/718,719,720,721,722,72 4,755.ccls. or 702/117,186.ccls. or 702/117,186.ccls.) and sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line\$1 or bit adj line\$1) with data with line and semiconductor\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with (multiplex\$3 or select\$3) with bit with line\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01	

		2004/11/01 15:11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("5654926").PN.	2)	IS&R	39
		2004/11/01 14:20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	5870343.uref.	9	BRS	သ 8
		2004/11/01 14:20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	"5870343"	œ	BRS	37
		2004/11/01 14:18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("5894440").PN.	2	IS&R	36
		2004/11/01 14:17	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("6049492").PN.	2	IS&R	ა 5
	·	2004/11/01 14:14	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	"20040085844"	2	BRS	34
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40	BRS	21062	,185.12,185.1 21,185.22,189 1,196,198,201 210,230.03,23	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02 14:36		
₽ -	BRS		(365/185.2,149,185.12,185.17,185.18,185.21,185.22,18 9.01,189.09,191,196,198,20 1,203,207,208,210,230.03,2 33.ccls.) and sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line\$1 or bit adj line\$1) with data with line and semiconductor\$1 and (precharg\$3) with electric\$4 with electric\$4 with (multiplex\$3 or select\$3) with bit with bit with line\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/01		
42	BRS	22235	49,185.12,185.1 5.21,185.22,189 191,196,198,201 8,210,226,230.0	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02 14:40		
43	BRS	22290	365/109,185.2,149,185.12,1 85.17,185.18,185.21,185.22 ,189,189.01,189.09,191,196 ,198,201,203,207,208,210,2 26,230.03,233.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02 14:51		

		2004/11/02 15:13	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("5627789").PN.	2	IS&R	46
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		2004/11/02 15:13	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	("5010523").PN.	2	IS&R	4 5
		2004/11/02	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(365/109,185.2,149,185.12, 185.17,185.18,185.21,185.2 2,189,189.01,189.09,191,19 6,198,201,203,207,208,210, 226,230.03,233.ccls.) and sens\$3 with circuit\$1 with logic\$4 with (data or information) with memory and signal\$1 with (bit-line\$1 or bit adj line\$1) with data with line and semiconductor\$1 and (precharg\$3) with electric\$4 with (multiplex\$3 or select\$3) with bit with line\$1	ω	BRS	4 4
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47 BRS	ည ယ ဟ	sens\$3 with circuit\$1 and signal\$1 with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02 15:17		·
48 BRS	J	sens\$3 with circuit\$1 and signal\$1 with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4 with isolat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02 15:19		
49 BRS	7	sens\$3 with circuit\$1 and signal\$1 with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4 with isolat\$3 and data with line\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/11/02		

57 22	Д	50	
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sens\$3 with circuit\$1 and select\$3 with signal\$1 with (bit-line or bit adj line) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4 with high with voltage\$1 with isolat\$3 and data with line\$1	sens\$3 with circuit\$1 and select\$3 with signal\$1 with (bit-line or bit adjline) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4 with isolat\$3 and data with line\$1 and high with voltage\$1	sens\$3 with circuit\$1 and select\$3 with signal\$1 with (bit-line or bit adjline) with semiconductor\$1 with memory and (precharg\$3 or pre-charg\$3) with electric\$4 and logic\$4 with isolat\$3 and data with line\$1	Search Text
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·		2004/11/02 15:42	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(precharg\$3 or pre-charg\$3) with (first adj second adj (bit-line\$1 cr bit adj line\$1)) and electric\$4 and logic\$4 with high with voltage\$1 with isolat\$3 and data with line\$1	 1	BRS	ហ ហ
		2004/11/02 15:39	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	\$3 with circuit\$1 charg\$3 or charg\$3) with fir second with -line or bit adj electric\$4 and lo high with voltag isolat\$3 and dat line\$1	ω	BRS	44
-		2004/11/02	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	\$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$		BRS	53
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sens\$3 with circuit\$1 and (precharg\$3 or pre-charg\$3) with electric\$4 with (first or bit adj line\$1) and electric\$4 and logic\$4 electric\$4 and logic\$4 with high with voltage\$1 with isolat\$3 and data with line\$1	Search Text
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